

REMARKS

Claims 6-20 are presently pending. Claims 1-11, 14-17, and 20 are rejected. Claims 12, 13, 18, and 19 were objected to but indicated as allowable if rewritten in independent form. Assignee appreciates Examiner's indication of allowable subject matter. Claims 1-5 are cancelled without prejudice. Claims 7, 10, 12, and 13 are amended.

Claims 7, 10, 12, and 13 were objected to for informalities. The foregoing claims are amended and in view of the amendments, it is submitted that the objections are overcome.

Claim 6 was rejected under 35 U.S.C. 102(e) as anticipated by Ishibashi. Ishibashi is directed to an "Image Information Transmission System". Ishibashi, Title. In Ishibashi, "The video data decoded by the DVD decoder 112, for use in the interlace display, is directly input to the digital video input port of the VGA controller 113 through an exclusive video bus. The video bus is, for example, a ZV (Zoomed Video) port comprising a digital YUV data signal line having a width of 16 bits corresponding to the 422 image format, (luminance data Y of 8 bits and chrominance data UV of 8 bits), and signal lines for transmitting horizontal and vertical signals (HSYNC, VSYNC) and a pixel clock (CLK)." Ishibashi, Paragraph 42.

"In the vertical blanking period, the video port controller 502 outputs the contents of the video control data set in the video control data register 112a to the video port of the VGA controller 113 through the digital YUV signal line."

MPEP 2131 states, "[a] claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference." *Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628, 631 2 USPQ2d 1051, 1053 (Fed.Cir. 1987). MPEP 2131 also states, "[t]he identical invention must be shown in as complete detail as is contained in the ... claim." *Richardson v. Suzuki Motor Co.*, 868 F.2d 1226, 1236, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989).

Claim 6 is directed to communicating data over a communications link comprising shortening a blanking period in the data to accommodate auxiliary data". Assignee respectfully submit that Ishibashi does not teach or fairly suggest "shortening a blanking period" as claimed, among other limitations, in claim 6. Emphasis Added. In contrast, Ishibashi merely teaches that "during a blanking period of a video signal transmitted from a DVD decoder to a video input port of a VGA controller, display control information, such as video control data or a command, is transmitted to the video input port of the VGA-controller through a video signal line for transmitting the video signal." Ishibashi, paragraph 10 (Emphasis Added).

Thus, wherein Examiner states that "Ishibashi describes a system/method of communicating data over a communications link which uses (shortens) a blanking period in the data to accommodate auxiliary data (paragraph 10)," (Emphasis Added), Assignee respectfully traverse the rejection for the foregoing reasons and requests that Examiner withdraw the rejection to claim 6, as well as dependent claims 7-19.

Examiner has rejected claims 7 and 10 under 35 U.S.C. 102(e) as anticipated by Ishibashi. Claim 7 is directed to, among other limitations "modifying at least one HSYNC signal". Examiner has indicated that "Ishibashi further describes modifying the HSYNC signal to accommodate the auxiliary (video control) data (paragraph 94)...." Claim 10 is directed to, among other limitations, "modifying a VSYNC signal". Examiner has indicated that "Ishibashi further describes modifying the VSYNC signal when auxiliary data is to be transmitted (paragraph 94...)".

In Ishibashi, as noted above, "The video data decoded by the DVD decoder 112, for use in the interlace display, is directly input to the digital video input port of the VGA controller 113 through an exclusive video bus. The video bus is, for example, a ZV (Zoomed Video) port comprising a digital YUV data signal line having a width of 16 bits corresponding to the 422 image format, (luminance data Y of 8 bits and chrominance data UV of 8 bits), and signal lines

for transmitting horizontal and vertical signals (HSYNC, VSYNC) and a pixel clock (CLK)." Ishibashi, Paragraph 42 (Emphasis Added).

Ishibashi at paragraph 94 teaches that "It outputs the vertical sync signal (Vsync), horizontal sync signal (Hsync), pixel clock (CLK) and digital YUV data to the video port of the VGA controller 113. In the vertical blanking period, the video port controller 502 outputs the contents of the video control data set in the video control data register 112a of the video port of the VGA controller 113 through the digital YUV signal line. ... The video control data can be transmitted not only in the vertical blanking period but also in the horizontal blanking period." (Emphasis Added).

Thus, because Ishibashi teaches "a digital YUV data signal line ... and signal lines for transmitting horizontal and vertical signals (HSYNC and VSYNC)" and that the "video controller 502 outputs the contents of the video control data ... through the digital YUV signal line", Ishibashi does not teach or fairly suggest "modifying at least one HSYNC signal" as claimed, among other limitations, in claim 7 or "modifying a VSYNC signal", as claimed among other limitations, in claim 10. For the foregoing reasons, Assignee traverses the rejection to claim 7 and requests that Examiner withdraw the rejections to claims 7 and 10, as well as claims 11-19 that are dependent on claim 10.

Claim 20 was rejected under 35 U.S.C. 102(e) as anticipated by Ishibashi. Claim 20 includes among other limitations, "a reformatter adapted to shorten a blanking period" (Emphasis Added). Examiner has indicated that Ishibashi describes a "system for communicating data and auxiliary data over a video communications link, comprising: a reformatter (a decoder) adapted to shorten a blanking period in the data to accommodate auxiliary data, forming at least one frame (of 4 bits) (paragraph 58...". Ishibashi, paragraph 58 teaches, "During a vertical blanking period when the vertical sync signal (VSYNC) is high in level, in period of lines 2 and 264, video control data of four bits is output from the digital YUV data output terminal of the DVD decoder 112 ..." (Emphasis Added). Thus Assignee respectfully traverse where examiner indicates that Ishibashi describes

a reformatter (decoder) adapted to shorten a blanking period...", and requests that Examiner withdraw the rejection to claim 20.

CONCLUSION

For at least the foregoing reasons, each of the pending claims are allowable, thereby placing the application in a condition for allowance. Examiner is requested to pass this case to issuance.

The Commissioner is hereby authorized to charge any deficiency in the amount enclosed or any additional fees which may be required under 37 CFR 1.16 or 1.17 to Deposit Account No. 13-0017 in the name of McAndrews, Held & Malloy, Ltd.

RESPECTFULLY SUBMITTED,



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